## ABSTRACT OF THE DISCLOSURE

An apparatus and method for indicating and allowing hot swapping of a circuit
board. During both insertion and extraction of a circuit board from a system, two inputs
signals are generated from staggered pins located on the circuit board's connector. The
inputs are processed through a NAND function implemented with transistors and output
to two Schmitt trigger inverters connected in series. The output of the series connection
of Schmitt trigger inverters goes high when both input signals are high and goes low
when one of the inputs signals goes low. In addition, through the use of a resistor,
capacitor combination connected to the input of the first Schmitt trigger inverter, the
output signal remains high for a period of time after one of the input signals goes low.
This additional period of time prevents any damage or disruption of signaling caused by
transient current and voltage fluctuations as a circuit board is inserted or extracted. The
output signal can be used in both single-ended and differential SCSI applications.

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